

[Your Name]
[Your Position]
[Your Company/Organization]
[Your Address]
[City, State, Zip Code]
[Email Address]
[Phone Number]
[Date]
[Recipient Name]
[Recipient Position]
[Recipient Company/Organization]
[Recipient Address]
[City, State, Zip Code]

Dear [Recipient Name],

Subject: XNOR Gate Specifications

I am writing to provide you with the specifications for the XNOR gate as per our recent discussions.

****1. Description:****

The XNOR gate is a digital logic gate that outputs true or logic high (1) when the number of true inputs is even.

****2. Truth Table:****

A	B	Output (A XNOR B)
0	0	1
0	1	0
1	0	0
1	1	1

****3. Symbol:****

[Insert XNOR gate symbol image]

****4. Electrical Specifications:****

- Voltage Level: [Specify voltage levels]
- Current Rating: [Specify current limit]
- Input Impedance: [Specify input impedance]
- Output Impedance: [Specify output impedance]

****5. Application:****

The XNOR gate is commonly used in digital circuits for error detection and correction mechanisms.

Please feel free to reach out if you need any further information or clarifications regarding this specification.

Thank you for your attention.

Sincerely,

[Your Name]
[Your Position]
[Your Company/Organization]