

[Your Name]
[Your Address]
[City, State, Zip Code]
[Email Address]
[Phone Number]
[Date]

[Recipient Name]
[Recipient Title]
[Department Name]
[University Name]
[Address]

[City, State, Zip Code]

Dear [Recipient Name],

I am writing to propose my thesis project titled "[Proposed Thesis Title]" under the supervision of [Supervisor's Name] in the [Department Name] at [University Name]. My research aims to explore [briefly describe focus of the research, e.g., VHDL design methodologies, digital circuit optimization, etc.].

The objective of this thesis is to [state the main objectives or hypotheses]. I will utilize VHDL (VHSIC Hardware Description Language) to [describe the method and tools you will use, e.g., simulate circuits, implement designs, etc.].

I believe this research could contribute to [explain the potential impact, such as advancements in digital design, efficiency in hardware implementation, etc.]. The proposed timeline for this research is as follows:

- [Phase 1: Description and Duration]
- [Phase 2: Description and Duration]
- [Phase 3: Description and Duration]

I am eager to advance my knowledge in this field and look forward to your feedback on my proposal. Thank you for considering my request.

Sincerely,

[Your Name]
[Your Student ID] (if applicable)
[Department Name]
[University Name]